

XpressVUP - LP9PT3

Low-profile PCIe Network Processing FPGA Board Featuring 2 x 100G Ethernet

- Low-profile PCIe FPGA board with -3 speed grade Virtex Ultrascale+ VU9P FPGA
- PCIe Gen3 x16
- 2 banks of DDR4, 8GByte total
- 2 banks of QDR2+, 576 Mbit total
- Dual QSFP28 cages 10GbE/40GbE/100GbE
- Dual slot active heatsink



Target applications:

Finance
Acceleration
Data center
Networking
Security

Contact :

Europe Sales Office
sales@reflexces.com

www.reflexces.com

UltraSCALE™
Architecture



 **XILINX**®

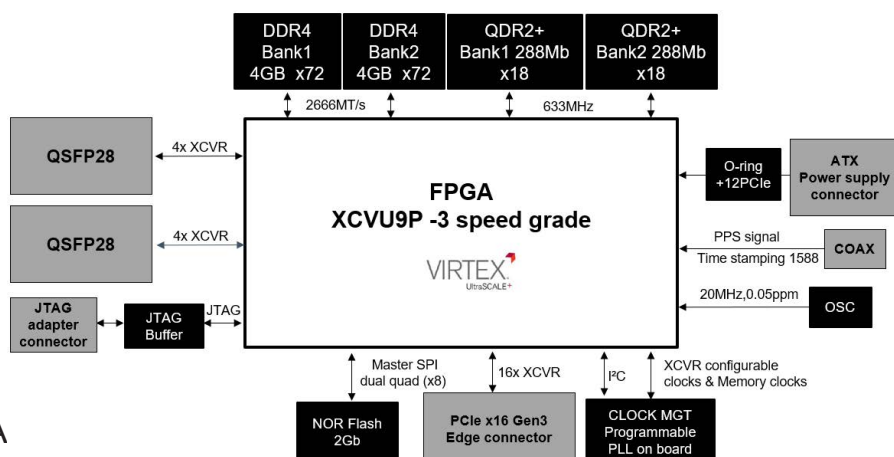
North America Sales Office
salesusa@reflexces.com



XpressVUP-LP9PT3

Benefits

- On board two DDR4 and two QDR2+ independent banks
- Two QSFP28 optical cage for multi networking solutions
- PCIe endpoint with 16 lanes at 8 Gb/s link rate (Gen3)
- Fail safe configuration flash sector
- Tandem configuration mode available
- JTAG connector
- Xilinx Virtex UltraScale+ VU9P-3 FPGA



Full Specifications

FPGA and Configuration Modules

- Xilinx Virtex UltraScale+ 16nm FPGA = XCVU9P-3FLGB2104E
- 2,6 M System Logic Cells, 270 Mb UltraRAM
- JTAG connector for external Xilinx USB cable
- 2x Nor Flash for dual quad SPI (x8) configuration mode

Communication Interfaces

- PCI Express x16 (Gen 1, 2 or 3)
- 2 x QSFP28 quad optical cage (2 x 4 XCVR : 28 Gb/s per link)
 - 10 GbE/25GbE/50GbE/100GbE/2 x 100GbE networking interface
 - Other protocols supported by the QSFP28 modules

Memory

- On board DDR4, 2x banks by 64-bit + 8 bits ECC, total 8GB @2666 MHz
- On board QDR-II+, 2x banks by 18-bit, total 576Mbit @550MHz

Operating Range

- 10°C to 45°C

Deliverables

- Low-profile (half-height) form factor PCIe Board
- PCIe standard full height and low profile brackets
- ATX power cable and expander
- JTAG adapter cable to USB
- XpressVUP Board Support Package
 - Reference Manual
 - HDL designs, Vivado 2023.1
 - PCIe Reference Design using AMD Xilinx QDMA IP
- Mechanical files: 3D DWF/STEP models and 2D CAD drawing (upon request)

Ordering information

Part number	Description
▪ XpressVUP-LP9PT3	▪ VU9P speed grade -3; 2 bank DDR4 by 72bit of 8GBytes total @2666MT/s; 2 banks QDR2+ by 18bit of 288Mbit each @550Mhz

Other resources

- On board programmable PLL oscillator (Si5345), highly flexible and configurable clock generator.
- On board high precision oscillator provides clock accurate 20MHz-0.05ppm for Precision Time Protocol (PTP) Ethernet, synchronisation protocol standardized IEEE 1588
- One coax connector for PPS (pulse per second) that allows a synchronisation of multiple electronics parts

Board Dimensions

- Length : 167.7mm (6.59inches)
- Height : 68.9mm (2.71 inches) / Low Profile PCIe
- Dual slot active heatsink

Standards and compliance

- RoHS/REACH compliant
- UL certified
- ISO9001 Facility